

Hamming Error Correction Example

Suppose an 8-bit word stored in memory is 1100 0010. Using the Hamming Algorithm, determine what check bits would be stored in memory with data word?

Step ①: Determine the number of check bits needed:

$$2^k - 1 \geq M + k$$

k = # of check bits
 M = length of word

Let's $k=3$

$$2^3 - 1 \not\geq 8 + 3$$

Let's $k=4$

$$2^4 - 1 \geq 8 + 4 \checkmark \Rightarrow \text{So } k = 4. \text{ (minimum } k\text{).}$$

Step ②

$$\begin{aligned} \text{Code Word} &= M + k \\ &= 8 + 4 \\ &= 12 \text{ bits} \end{aligned}$$

Bit Positions	12	11	10	9	8	7	6	5	4	3	2	1
Binary Rep.	1100	1011	1010	1001	1000	0111	0110	0101	0100	0011	0010	0001
	D_8	D_7	D_6	D_5	C_8	D_4	D_3	D_2	C_4	D_1	C_2	C_1

Step ③: Check bits Calculation:

$$C_1 = \text{XOR all Data bits } \text{XXX1}$$

$$= D_1 \oplus D_2 \oplus D_4 \oplus D_5 \oplus D_7$$

$$C_2 = \text{XOR all Data bits } \text{XX1X}$$

$$= D_1 \oplus D_3 \oplus D_4 \oplus D_6 \oplus D_7$$

$$C_4 = \text{XOR all Data bits } \text{X1XX}$$

$$= D_2 \oplus D_3 \oplus D_6 \oplus D_8$$

$$C_8 = \text{XOR all Data bits } \text{1XXX} = D_5 \oplus D_6 \oplus D_7 \oplus D_8$$

$D_8 D_7 D_6 D_5 D_4 D_3 D_2 D_1$
 1 1 0 0 0 0 1 0

A	B	F
0	0	0
0	1	1
1	0	1
1	1	0

$$C_1 = 0 \oplus 1 \oplus 0 \oplus 0 \oplus 1 = 0$$

$$C_2 = 0 \oplus 0 \oplus 0 \oplus 0 \oplus 1 = 1$$

$$C_4 = 1 \oplus 0 \oplus 0 \oplus 1 = 0$$

$$C_8 = 0 \oplus 0 \oplus 1 \oplus 1 = 0$$

$C_8 C_4 C_2 C_1$
 0 0 1 0

⇒ Code word stored in memory: $D_8 D_7 D_6 D_5 C_8 D_4 D_3 D_2 C_4 D_1 C_2 C_1$
 1 1 0 0 0 0 1 0 0 1 0
 C 1 2 H

* Suppose now that data bit 3 sustains an error and changed from 0 to 1. When check bits re-calculated, we have:

$$\left. \begin{aligned} C_1 &= 0 \oplus 1 \oplus 0 \oplus 0 \oplus 1 = 0 \\ C_2 &= 0 \oplus 1 \oplus 0 \oplus 0 \oplus 1 = 0 \\ C_4 &= 1 \oplus 1 \oplus 0 \oplus 1 = 1 \\ C_8 &= 0 \oplus 0 \oplus 1 \oplus 1 = 0 \end{aligned} \right\} \text{new check bits}$$

⇒ When the new check bits compared with stored (old) check bits, the syndrome word is formed:

$$\begin{array}{cccc} & C_8 & C_4 & C_2 & C_1 \\ \oplus & 0 & 0 & 1 & 0 \\ & 0 & 1 & 0 & 0 \\ \hline & 0 & 1 & 1 & 0 \end{array}$$

⇒ result is 0110 indicating that bit at position 6 (i.e. D_3) is in error.